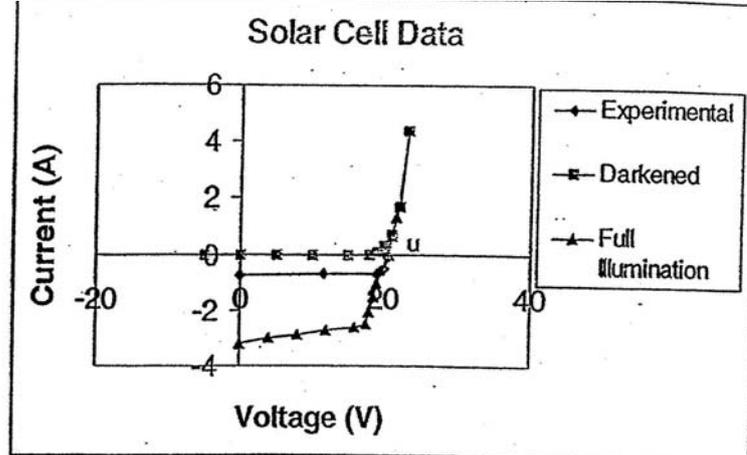


3.15 - Problem Set 4 Solutions

Problem 1

a.



b.

The suggested way of looking at this problem was to realize that photocurrent is proportional to the light intensity. The intensity ration will then just be the ratio of short circuit currents.

$$\frac{I_{SC,illum}}{I_{SC,Expt}} = \frac{3.1A}{0.72A} = 4.3$$

So the sun must be around 4.3 times brighter in order to reach full illumination. This is possible during the summer months. Many of you also made the point that because of the damaged condition of the solar cell that the cell will never reach full illumination which is probably true.

c.

Maximum power is determined by the largest possible IV product.

$$P_{max} = (IV)_{max} = 0.67A \times 18.9V = 12.7W$$

Operation time is then simply the desired energy divided by the maximum power.

$$t = \frac{\text{Energy}}{P_{max}} = \frac{1kWhr}{12.7W} = 79\text{hours}$$

For full illumination, the best power is 43.8 W. We want to match the load resistance such that the voltage dropped across the load corresponds exactly to the voltage supplied by the cell. This is an impedance matching problem.

Resistance is then found by taking the voltage at maximum power and dividing by current at maximum power.

$$R_{load,opt} = \frac{V}{I} = \frac{17.3V}{2.53A} = 6.8\Omega$$

For comparison, let's take a 40W light bulb.

$$P = \frac{V^2}{R} \Leftrightarrow R = \frac{V^2}{P} = \frac{(120V)^2}{40W} = 360\Omega$$

The optimal load of our solar cell is much less than for a typical light bulb.

e.

Connecting the 36 cells in series or parallel will change the voltage and current applied to the load.

With the cells connected in parallel, P_{max} is the same but V is 1/36 of the original value and I is 36 times the original value.

$$R_{parallel} = \frac{R_{series}}{(36)^2} = 5.3m\Omega$$

In parallel, the cell can be used to power low impedance devices.

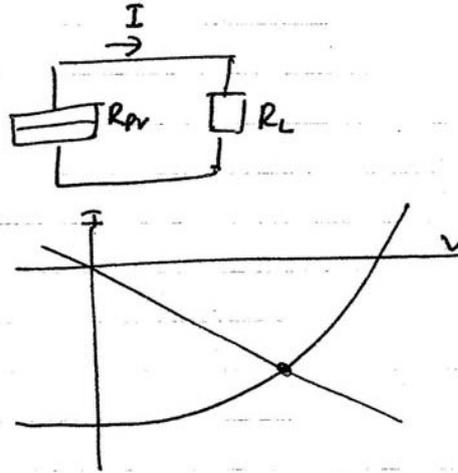
f.

The difference in current is due mainly to the large difference in junction area between a diode and the solar cell. Current densities are roughly equivalent for both but the large area of the solar cell allows for a much larger current from relations $V = IR$ and $R = \rho l/A$.

g.

The J_0 value \Rightarrow mobility is $\approx 900 \text{ cm}^2/\text{Vs}$. This is high \Rightarrow need good quality Si.

Problem 2



$$I = I_0 \left(\exp\left(\frac{eV}{kT}\right) - 1 \right) + I_g$$

$$I_0 = 10^{-6} \text{ A}, I_g = -2 \text{ A}$$

$$(R_{PV} + R_L) = V/I$$

I (A)	V	IV
2	0	0
0	0.38	0
0.5	0.37	0.19
1	0.36	0.36
1.5	0.34	0.51
1.7	0.33	0.56
1.8	0.317	0.57
1.9	0.299	0.5

For $I = 0$:

$$10^{-6} \exp\left(\frac{eV}{kT}\right) - 10^{-6} - 2 = 0$$

$$\exp \frac{eV}{kT} = 2 \cdot 10^6 = 14.5$$

$$eV = 0.025 \times 14.5 = 0.38V$$

In general:

$$V = 0.025 \ln \left(10^6 \times (2 - I) \right)$$

$$I = 0.5A$$

$$V = 0.37V$$

Max power when $I = 1.8A$, $V = 0.317$, gives $0.57W$. For load resistance: $R_{PR} + R_L = 0.317/1.8 = 0.18\Omega \Rightarrow R_L = 0.08\Omega$ since $R_{PR} = 0.1\Omega$.

b.

Cell has limited efficiency. Solar spectrum has range of colors, and the higher energy photons waste all energy $> E_g$. Not all of surface can absorb light due to conductor lines. Not all light absorbed within L_n or L_p or junction surface might be reflective.

Problem 3

There are many possible solutions to this problem. A number of factors must be considered:

The principle of operation of a pin diode relies on a large amount of light being absorbed in the i-region, which means that this region should be wider than any other layer through which light passes. However, a shorter W_i will enable charge carriers to cross the region in less time. $f_{max} \propto \frac{1}{W_i}$

When used in conjunction with a resistor, an RC time constant also affects the frequency response $\propto W_i$ (considering the pin structure to be similar to a capacitor).

The device will operate at frequencies up to a certain cut-off, limited by those considerations.

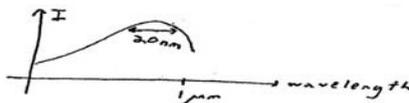
Devices of smaller width may have trouble absorbing longer wavelengths. To compensate for this, we can employ materials layers that help confine the light to the i-region by reflection.

Device doping must be arranged such that during operation, the entire i-region is depleted.

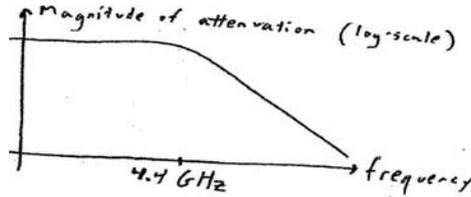
One possible solution is given as follows:

If we assume $N_D = 10^{10} \text{cm}^{-3}$ in the n-region and $N_A = 10^{19} \text{cm}^{-3}$ in the p-region, then we see that the i-region will deplete under the application of a small reverse bias. Considering the p⁺-i junction, $x_d = \sqrt{\frac{2E_R E_0}{4N_D}(V_{b_i} - V)}$; $x_d \approx 23 \mu\text{m}$
 $\Rightarrow V \approx 3.3$ volts.

We will set the n⁺ and p⁺ regions to be short, to reduce light absorption, but long enough to support i-depletion. $W_{n^+} = W_{p^+} = 1 \mu\text{m}$. Neglecting adsorption of incident light in the p⁺/n⁺ regions, and assuming that incident photons are contained by the internally reflective layers, the wavelength range over which this device is effective is determined by the band structure. The optimum wavelength will be just lower than $\lambda_g = \frac{1.24}{E_g} = 1 \mu\text{m}$.



The expected frequency response will attenuate signals above the cut-off frequency $f_{max} = \frac{V_{sat}}{W_i} = 4.4\text{GHz}$.



To estimate minimum detectable light intensity, note that the photogenerated current must exceed the minimum current that can be detected by whatever device we use to measure it. This will be limited by noise in the system. If we assume a typical minimum current of $I_{min} = 10^{-7}\text{A}$, then the corresponding light intensity is given by:

$$10^{-7}\text{A} = 10^{-7} \frac{\text{coulombs}}{\text{second}} = q(n+p) \frac{\text{e-h pairs}}{\text{second}}$$

$$n_{ph} = n = p = \# \text{ photons}$$

$$\frac{n_{ph}}{\text{second}} = 6.25 \cdot 10^{11}$$

$$\frac{n_{ph}}{\text{s} \cdot \text{area}} = 6.25 \cdot 10^{13} \frac{\text{photons}}{\text{s} \cdot \text{cm}^2}$$

Assume:

$$E \approx 1.1 \frac{\text{eV}}{\text{photon}}$$

$$\text{Intensity} = 6.25 \cdot 10^{13} \cdot 1.1 \cdot 1.6 \cdot 10^{-19} = 1.1 \cdot 10^{-6} \frac{\text{Watts}}{\text{cm}^2} \text{ by conservation of energy}$$

When optimally designed, a p-i-n diode will absorb as much light as possible in its completely depleted i-region so that generated carriers can be immediately accelerated to v_{sat} .

$$f_{mas} = \frac{1}{\frac{W_i}{v_{sat}}}, W_i = \text{intrinsic layer thickness}$$

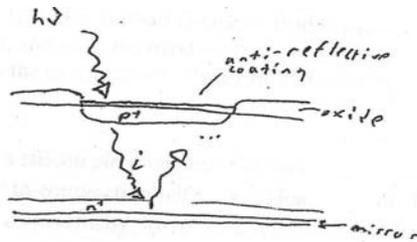
Smaller $W_i \Rightarrow$ faster frequency response, however if we continue to decrease W_i , the frequency response will become dominated by the RC time constant of the electrical circuit.

$$f = \frac{1}{RC} = \frac{1}{R \frac{E_0 E_R W_i}{A}} = \frac{W_i}{R E_0 E_R A}$$

To find optimum intrinsic layer thickness with the given R:

$$\frac{v_{sat}}{W_i} = \frac{W_i}{R E_0 E_R A} \Rightarrow \sqrt{V_{sat} R E_0 E_R A} = 23 \mu\text{m}$$

Note also that a wider W_i would allow more distance over which the light can be absorbed by the Si however we can make up for this by employing an anti-reflective coating at the top of the device and a mirror at its bottom, to confine and absorb incident radiation.



Considering optimal doping, we know that the i-region must be very lightly doped and the n^+ and p^+ regions must be heavily doped in order to completely deplete the i-region in equilibrium (more necessarily, in reverse bias). Let us assume a minimum achievable dopant concentration in the i-layer of 10^{13}cm^{-3} donors.