

MIT OpenCourseWare  
<http://ocw.mit.edu>

6.189 Multicore Programming Primer, January (IAP) 2007

Please use the following citation format:

Saman Amarasinghe and Rodric Rabbah, *6.189 Multicore Programming Primer, January (IAP) 2007*. (Massachusetts Institute of Technology: MIT OpenCourseWare). <http://ocw.mit.edu> (accessed MM DD, YYYY).  
License: Creative Commons Attribution-Noncommercial-Share Alike.

Note: Please use the actual date you accessed this material in your citation.

For more information about citing these materials or our Terms of Use, visit:  
<http://ocw.mit.edu/terms>

Name: \_\_\_\_\_

*Instructions: Write out your answers clearly in the space provided. You may use both sides of the page if necessary. Please write legibly.*

**Question 1:** One trend that is helping the shift from superscalars to multicores is the “Memory Wall”, i.e. the increasing gap between processor and DRAM performance. How can multicores circumvent the memory wall issue better than superscalars?